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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10081963	02/21/2002	438	745	2812	1765 Utech

**APPLICANTS: Geist Jon;

**CONTINUING DATA VERIFIED:
JL** FOREIGN APPLICATIONS VERIFIED:
JL
2603/0157809PG-PUB DO NOT PUBLISH ☐RESCIND ☐Foreign priority claimed ☐ yes ☒ no
35 USC 119 conditions met ☐ yes ☒ no
Verified and Acknowledged Examiners's initials JL

ATTORNEY DOCKET NO.

TITLE: Method for TMAH etching of CMOS integrated circuits

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED

Assistant Examiner

CLAIMS ALLOWED

Total Claims

Print Claim for
O.G.

ISSUE FEE

Amount Due

Date Paid

DRAWING

Sheets Drwg.

Figs. Drwg.

Print Fig.

Primary Examiner

PREPARED FOR ISSUE

Application Examiner



TERMINAL

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